Marathwada Shikshan Prasarak Mandal's

Sunderrao Solanke Mahavidyalaya, Majalgaon

INTERNAL QUALITY ASSURANCE CELL

CRIETERION 2 – TEACHING-LEARNING EVALUATION

2.3 Teaching-Learning Process

2.3.1 Student centric methods, such as experiential learning, participative learning and problem solving methodologies are used for enhancing learning experiences using ICT tools

Details of Digital and Microprocessor Lab

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Department of Computer Science

8086 Microprocessor and Digital Electronics kits for practicals

8086 Microprocessor Kit:

8086 Microprocessor is an enhanced version of 8085 Microprocessor that was designed by Intel in1976.it is a 16 bit microprocessor having 20 address lines and 16 data lines that provides up to 1 MB storage. It consists of powerful instruction set, which provides operations like multiplication & division easily.

8086 Microprocessor trainer kit is proposed to smooth the progress of learning & developing design of 8086 Microprocessor from Intel. User can enter user programs in assembly languages. User verifies the programs through 7 segment display.Userfriendly firmware conforms facilitating the beginners learns operations of Microprocessor quickly.

Key Features of 8086 Microprocessor Trainer Kit

Dynalog India Limited: 8086(INTEL)

Operating frequency 6.144MHz crystal.

32KB-SRAM for user data.

16KB EEPROM (software monitor program).

2x16 Char LCD display.

Serial interfacing using 8251.

48 Programmable 1/0 Pins for (2 x 8255).

Three 16-bit programmable timer (8253/8254).

40-Pin FRC connector for Bus Extension.

20-Pin FRC connector Add-on Interface from 8255.

9 pin DB Connector for UART (RS232) interface.

Built in assembler & disassemble.

101 PC type keyboard to enter user address/data commands.

Software monitor for loading &executing program with break point facility.

Digital Electronics Kits:

1) Study of GATE
2) Study of Half Adder Using Gates
3) Study of Full Adder using Gates
4) Study of 4 Bit Substractor Using 1's Complement
5) Study of 4 Bit Adder/Substractor using 2's Complement
6) Study of Flip Flop
7) Study of 4 bit adder
8) Study of 3:8 Decoder
9) Study of 4:1 Multiplexer
10) Study of MOD 8 Synchronous counter.
11) Study of Shift Register.
12) Study of Digital to analog converter using R-2R ladder.

HEAD
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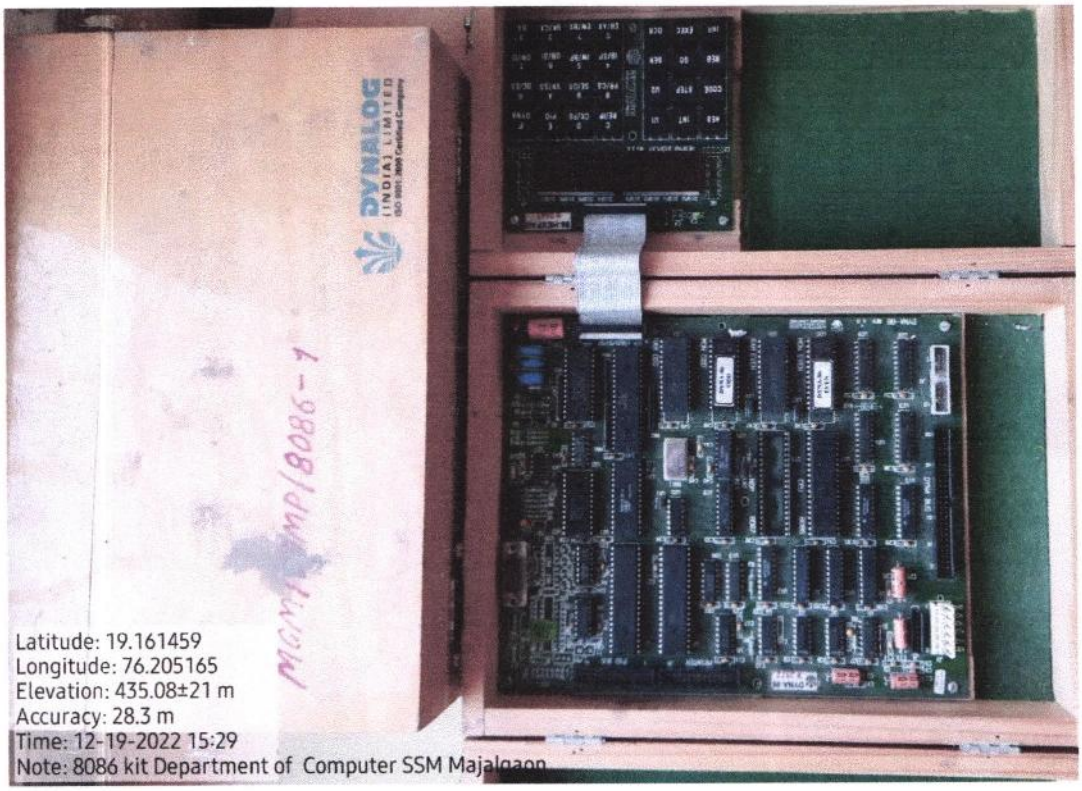
Computer Science

8086 Microprocessor Kit



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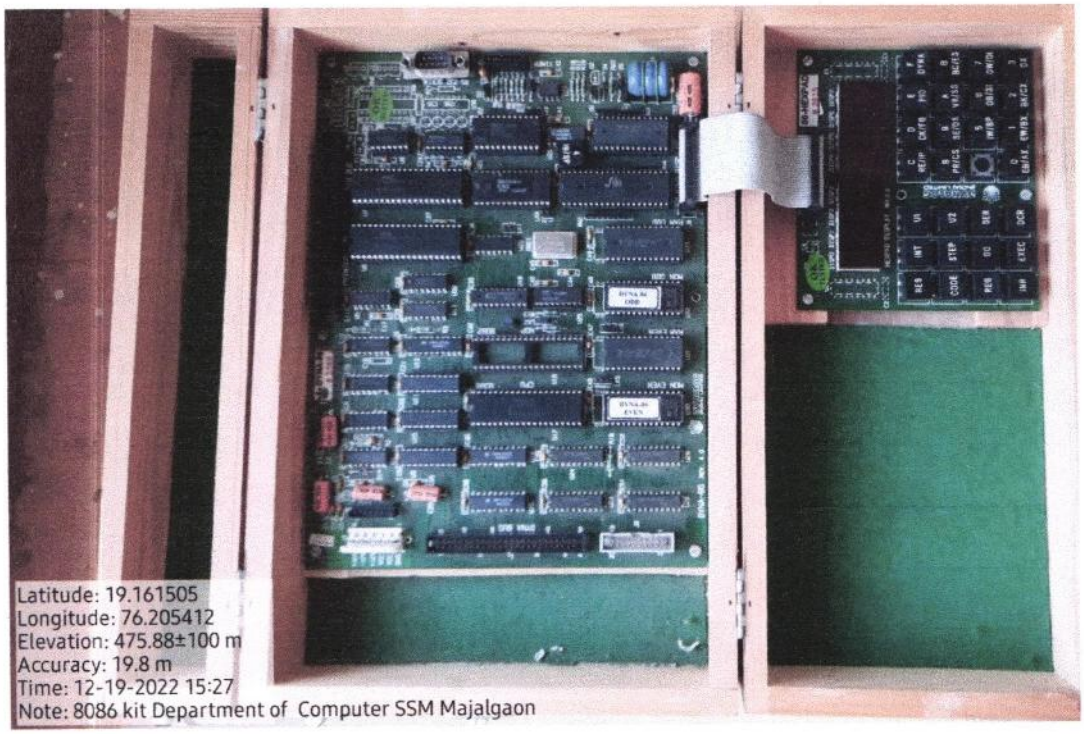

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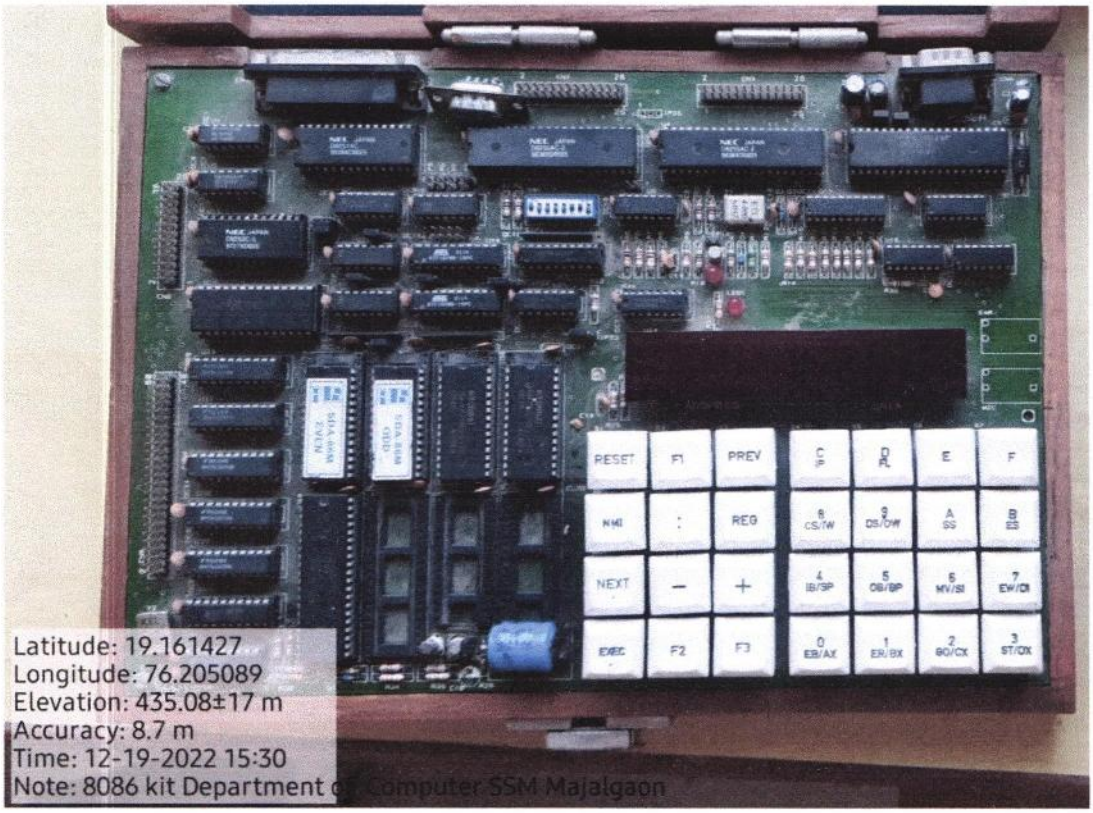

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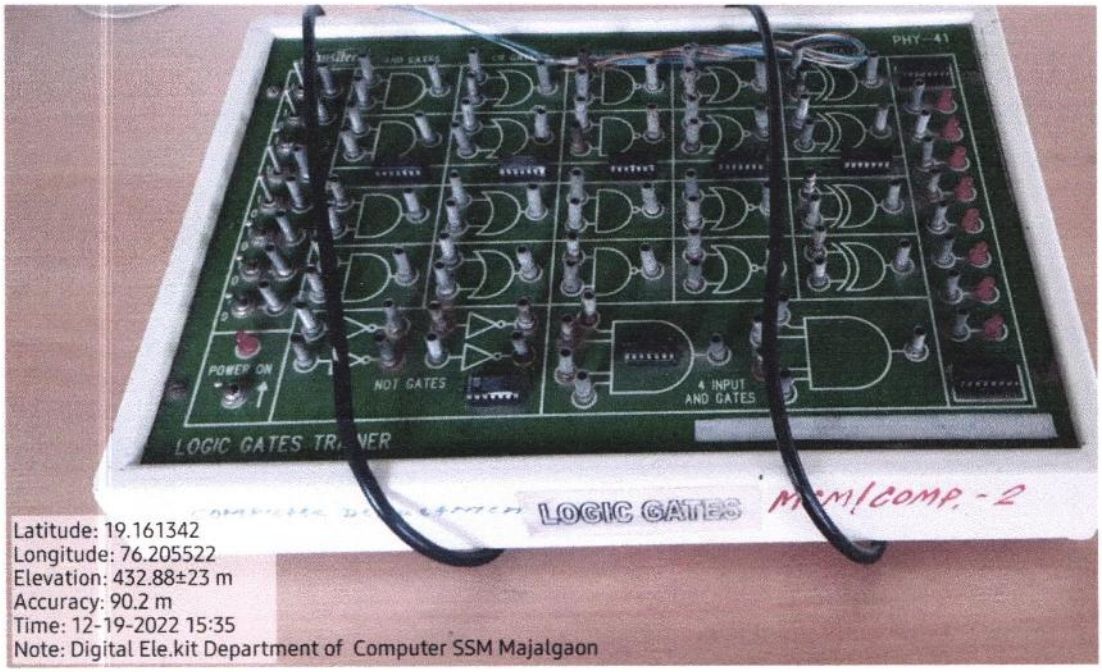

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Sanjay Sundarrao Salunke Mahavidyalaya Majalgaon Dist. Beed (M.S.) 431131
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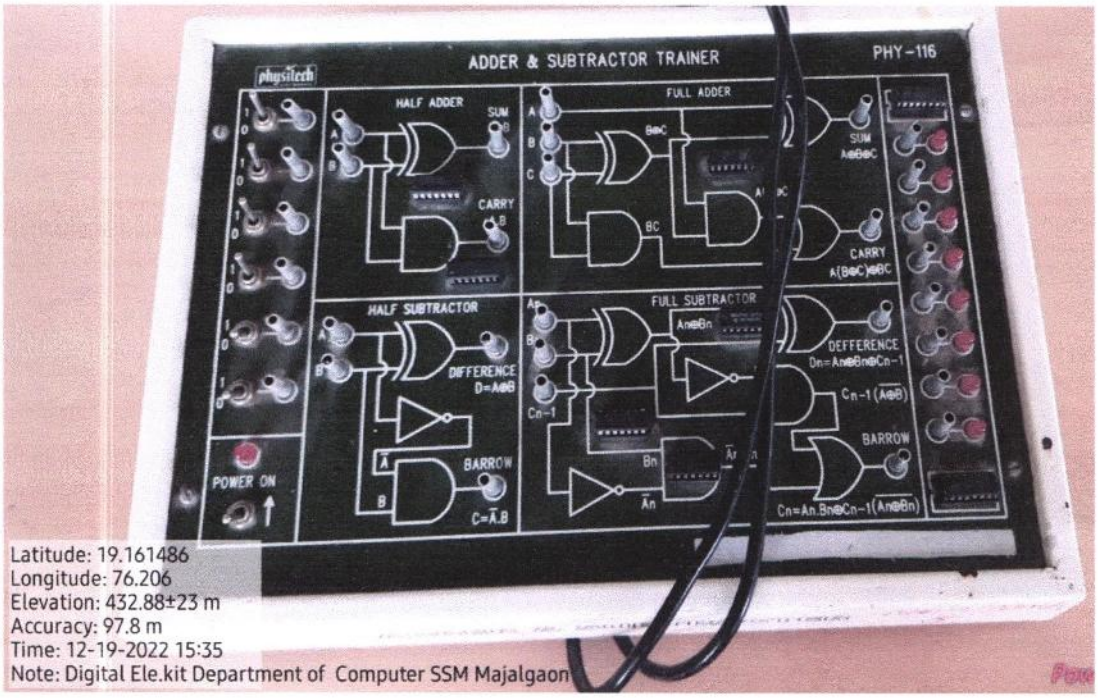

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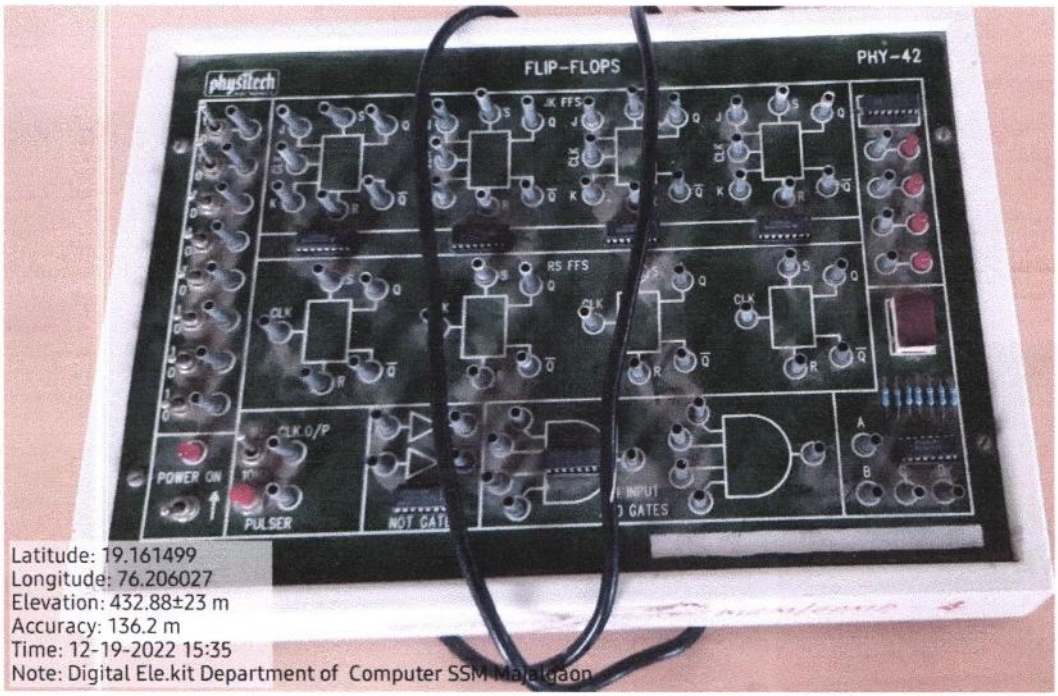

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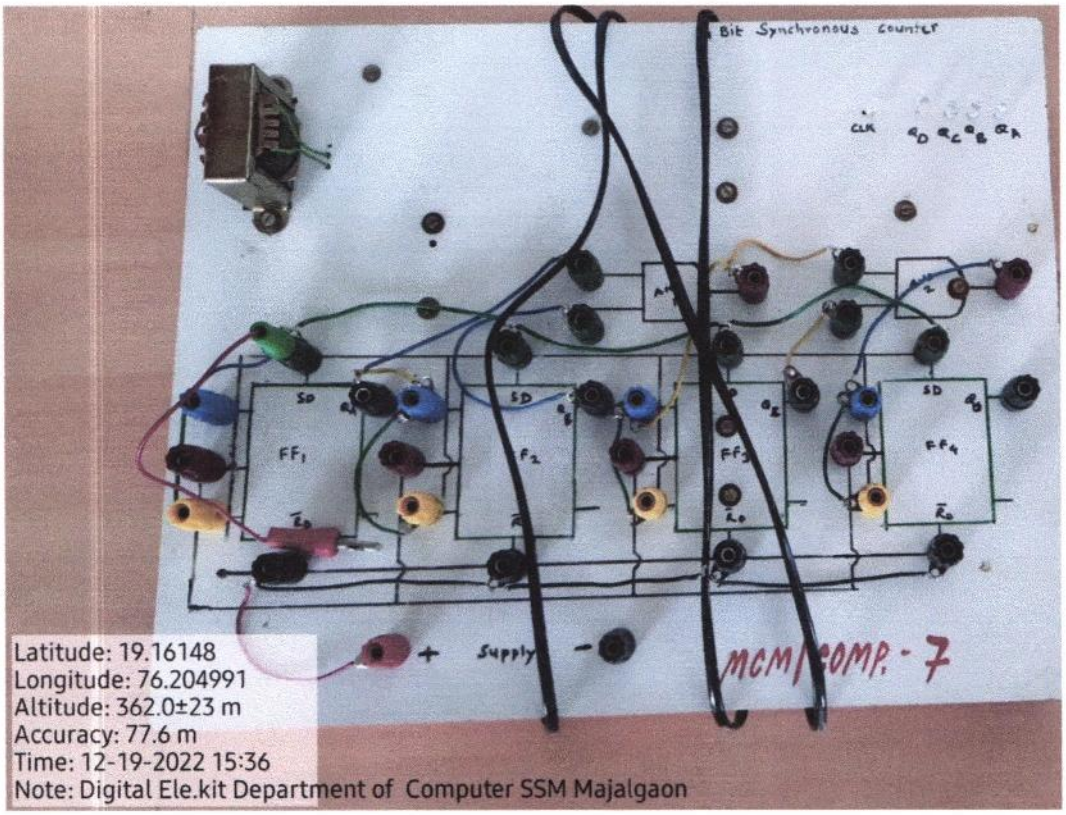

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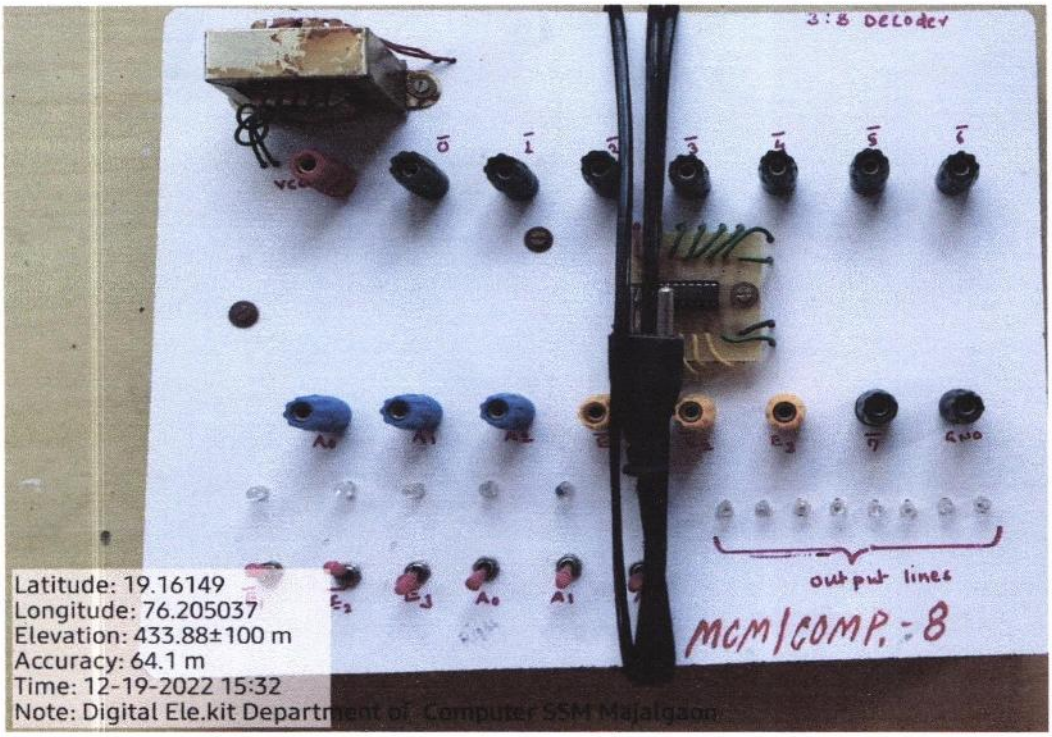

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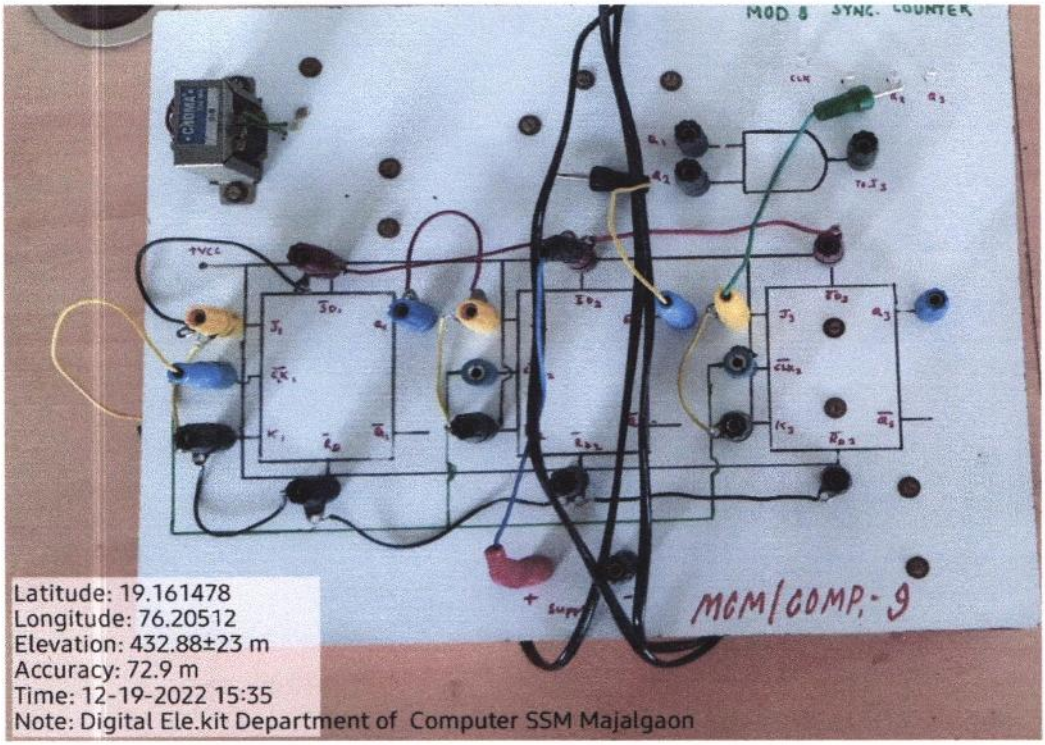

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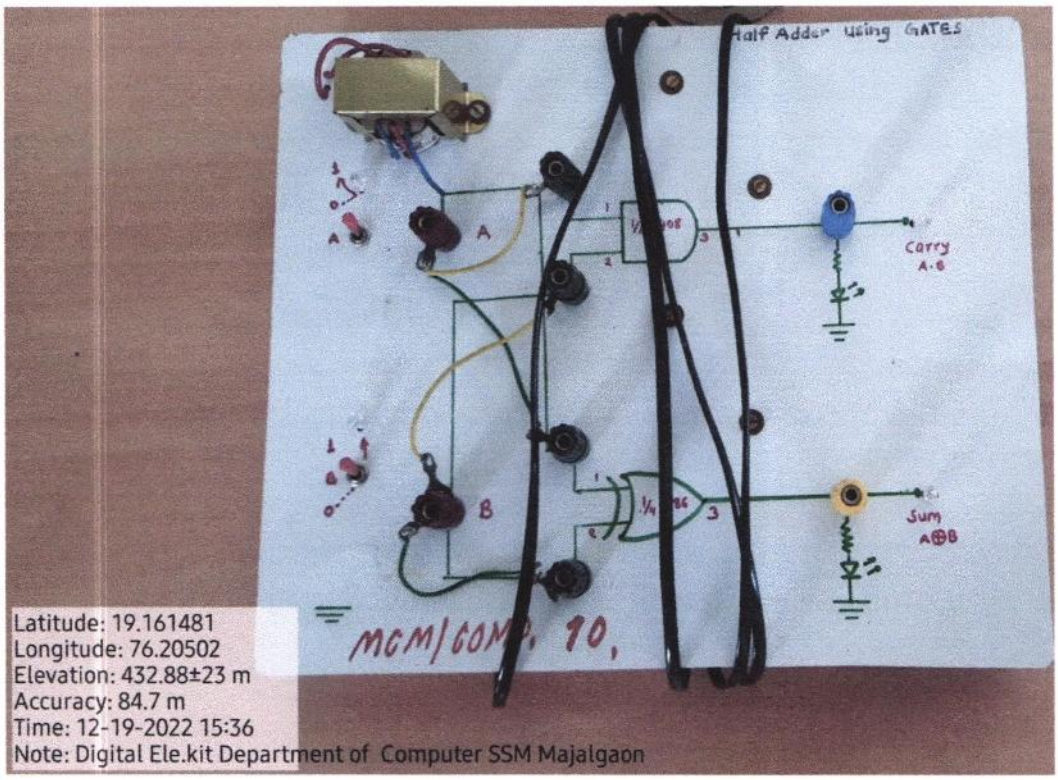

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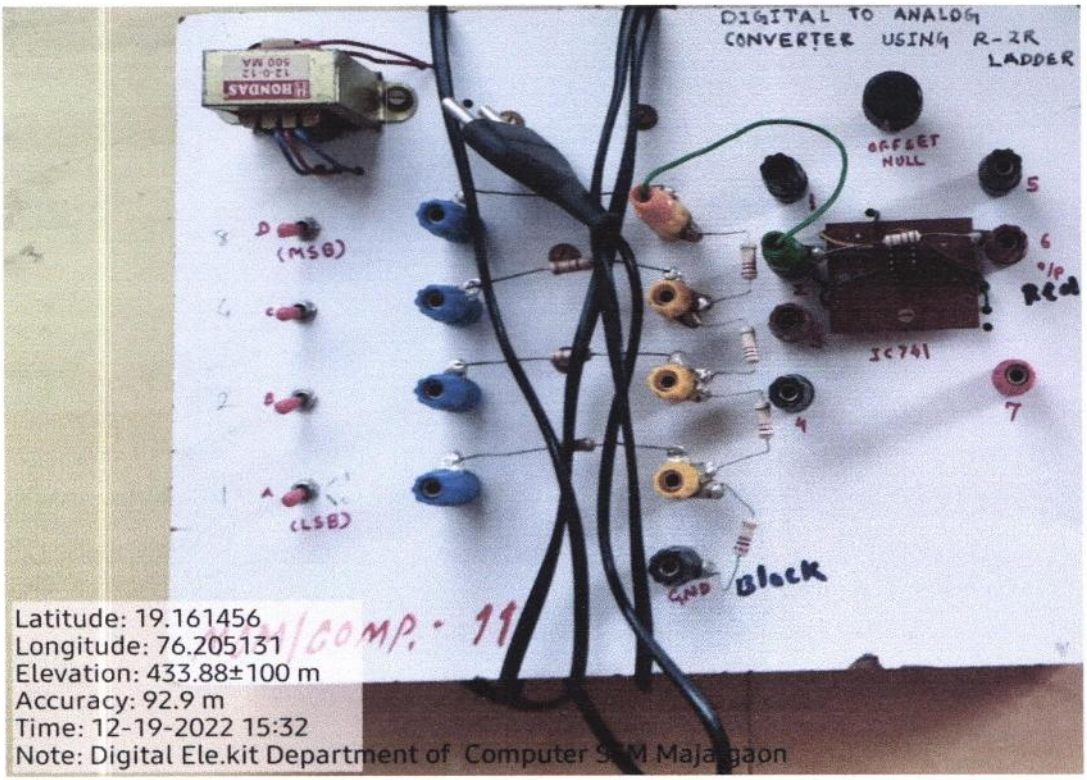

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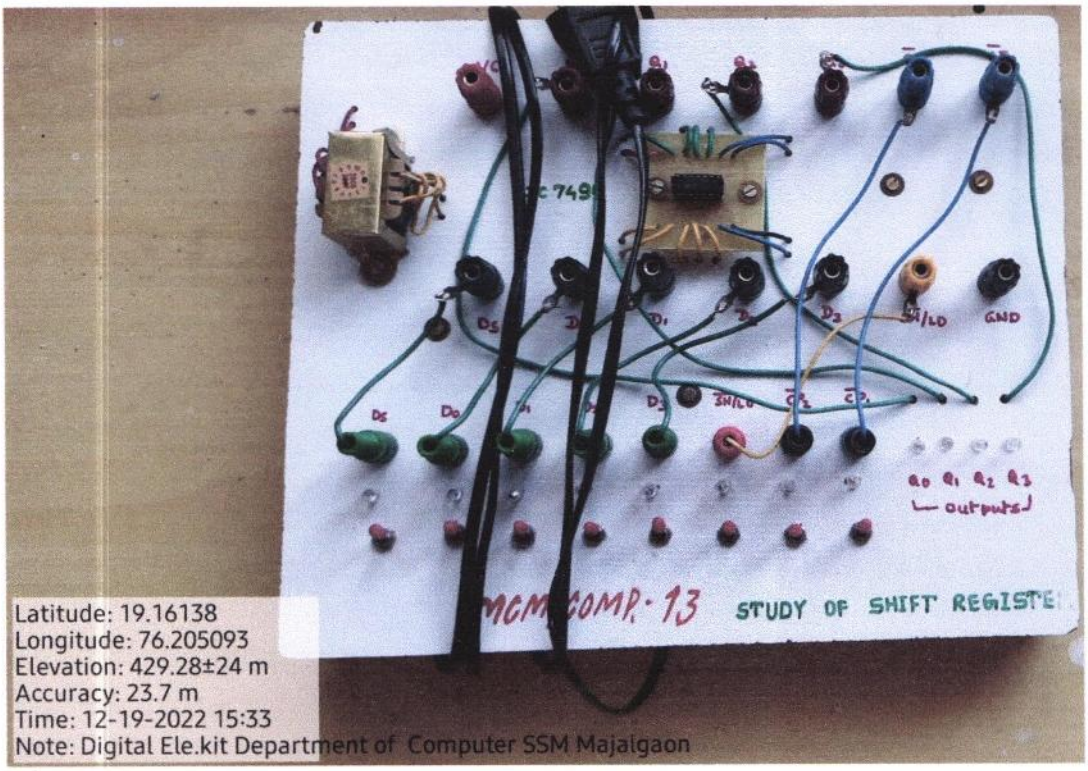

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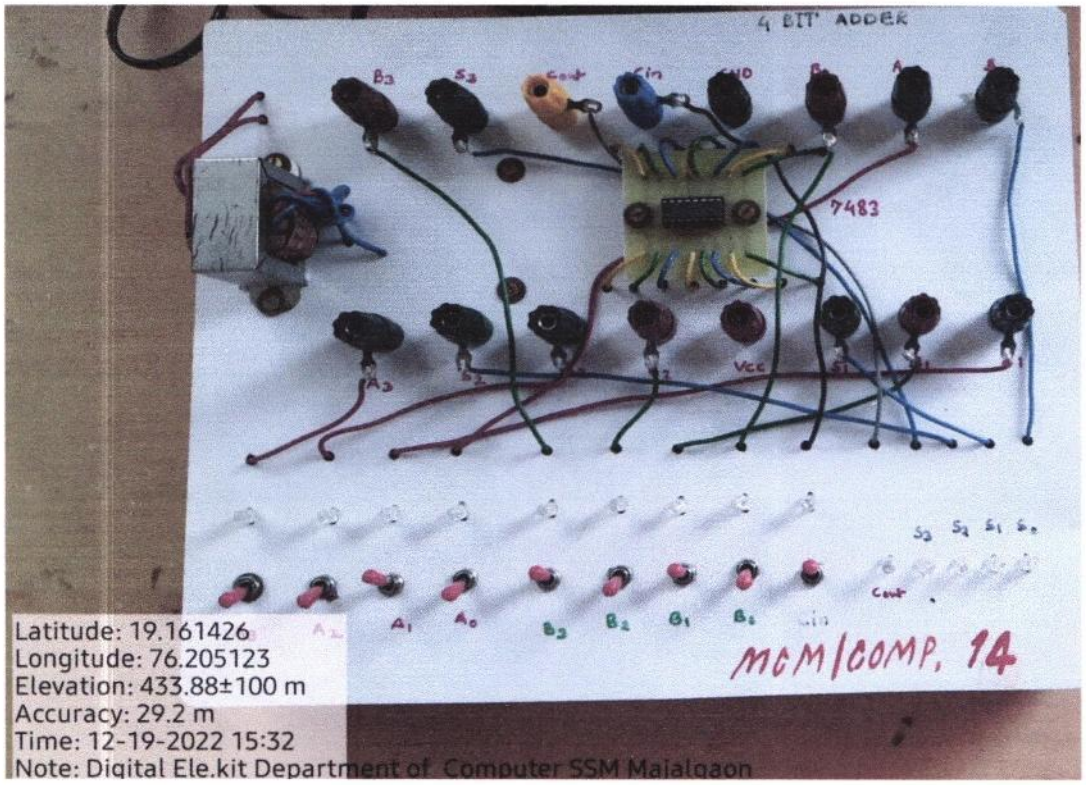

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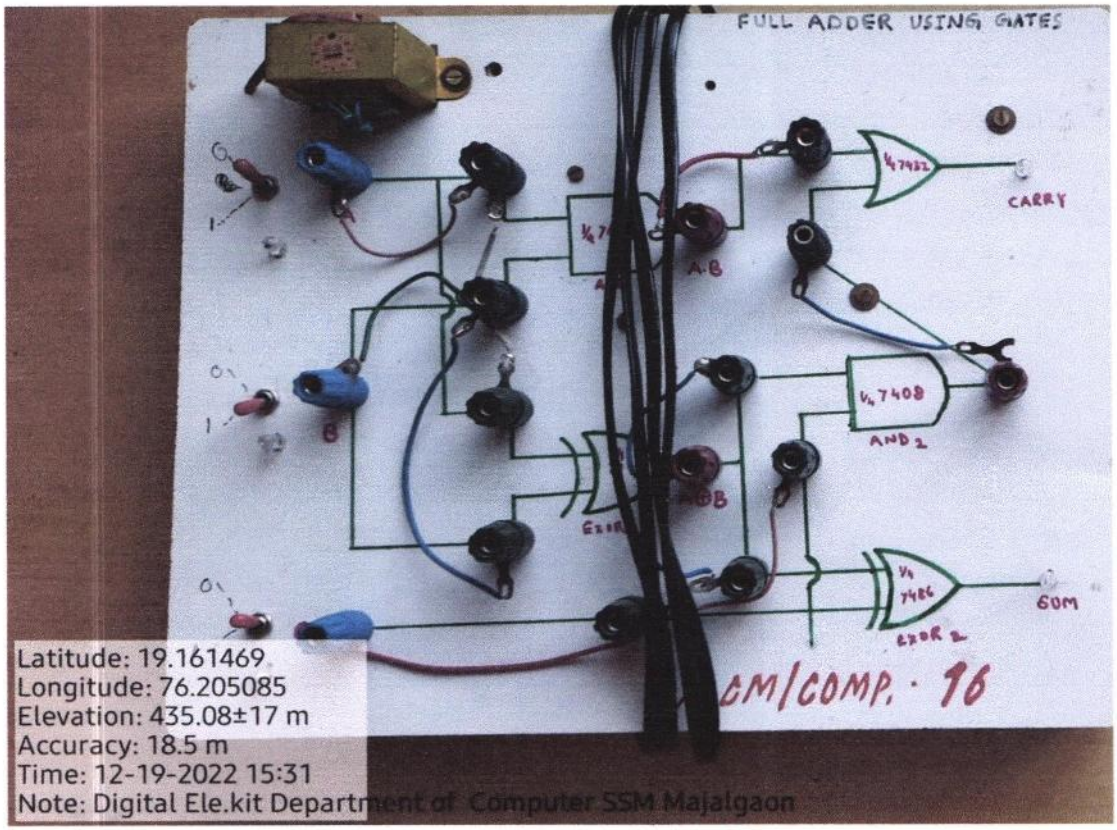

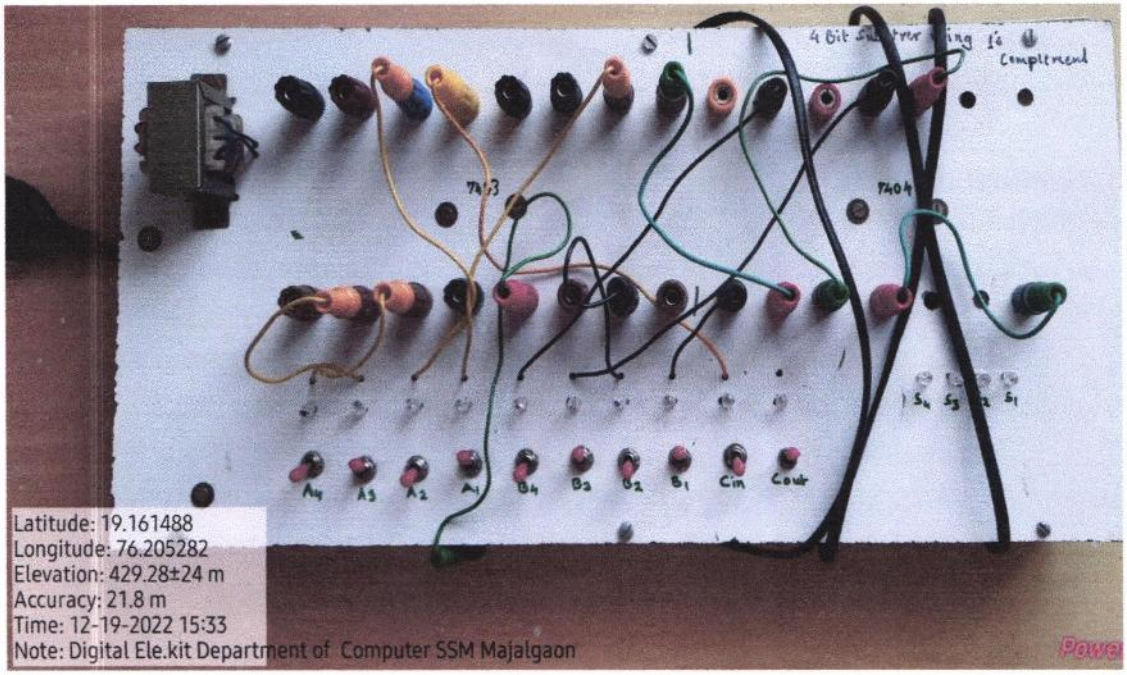

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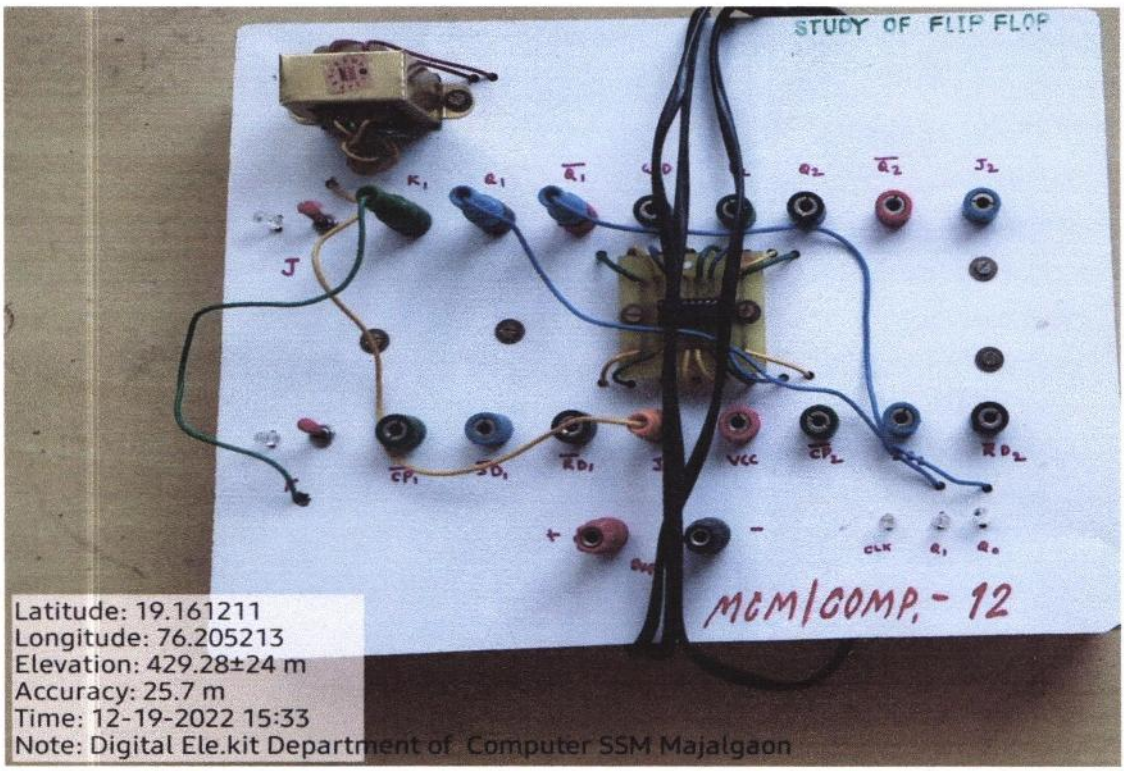

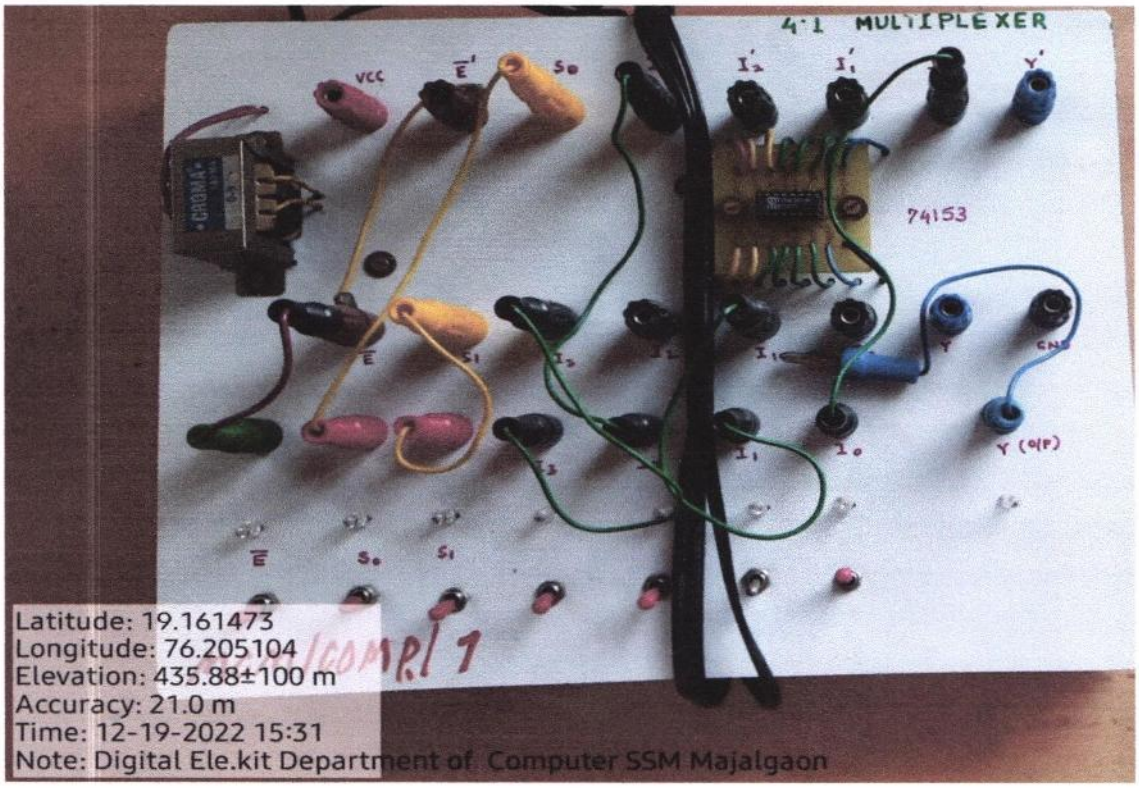

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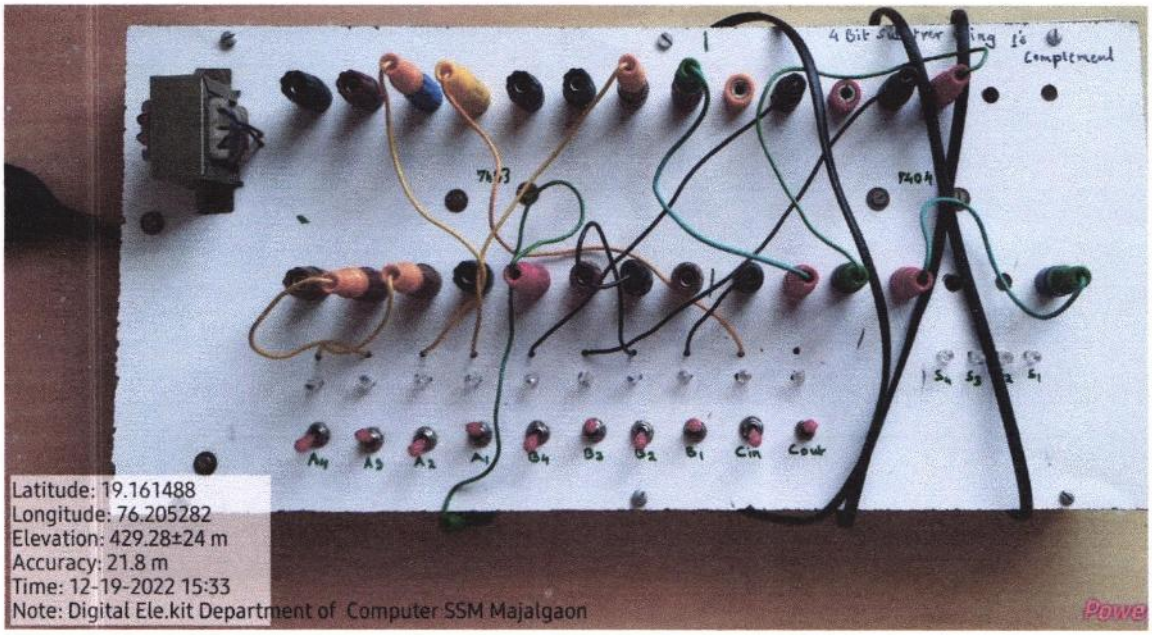

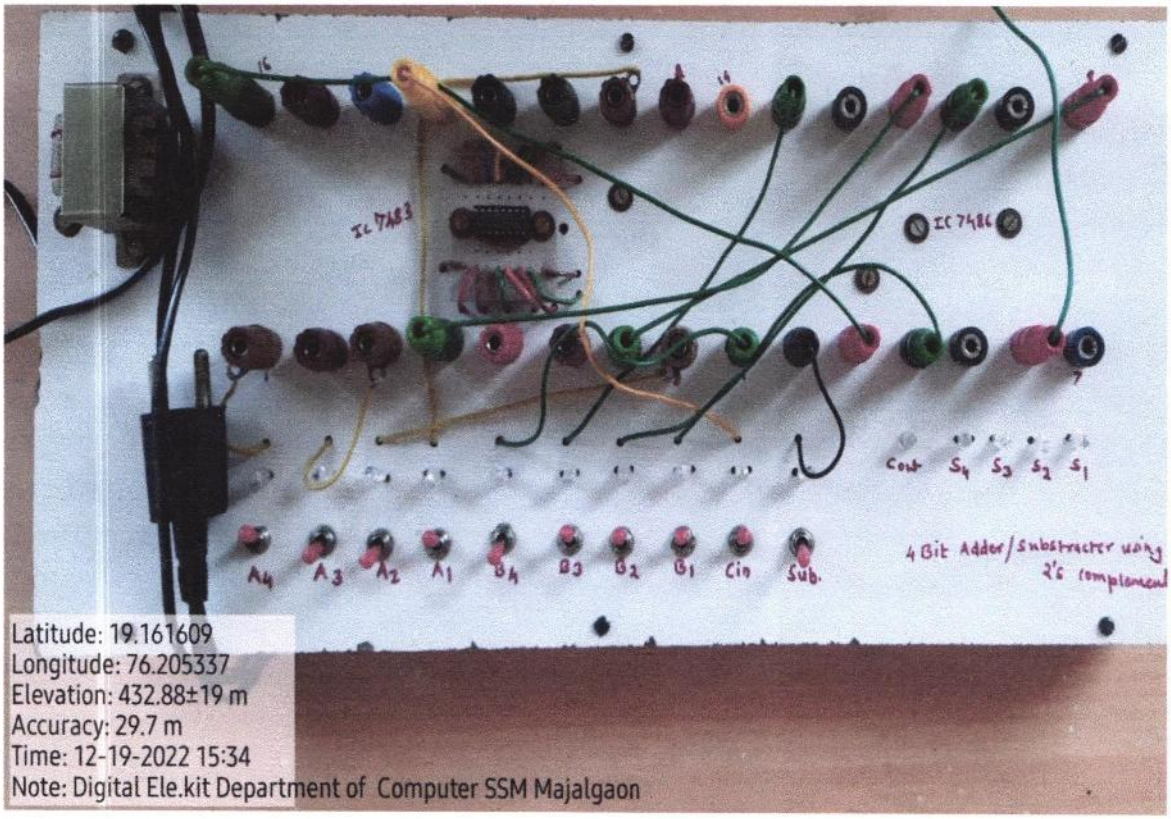

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Majalgaon, Dist.Beed (MS)

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PRINCIPAL
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